



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Jun Koyama, et al. Art Unit : 2871
Serial No. : 08/770,792 Examiner : H. Ngo
Filed : December 19, 1996
Title : SEMICONDUCTOR DEVICE HAVING A NONCONDUCTIVE MATERIAL
OR A WEAKLY CONDUCTIVE MATERIAL APPLIED TO A SIDE EDGE OF
A SUBSTRATE AND A METHOD OF FABRICATING THE SAME

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

1) Supplemental to an information disclosure statement filed in the application on May 9, 2003, Applicants submit the attached corrected Form PTO-1449. All of the documents listed on the form PTO-1449 were previously submitted with the information disclosure statement filed May 9, 2003. Therefore, no references are provided. A copy of the original initialed Form PTO-1449 is attached.

The corrected Form PTO-1449 is being submitted to correct typographical errors in the "Patentee" field or the "Publication Date" field of the references identified as Desig. IDs "AA, AD, and AP." Please note that the patentee previously entered in Desig. IDs "AA, AD, and AP" have properly been changed as follows:

Desig. ID	Changed from	Change to
AA	Kundo et al.	Kondo et al.
AD	17 Mar 1987	17 Mar 1978
AP	20 Apr 994	20 Apr 1994

Applicants respectfully request that the Examiner initial and return the corrected form PTO-1449.

2) Supplemental to an information disclosure statement filed in the application on June 17, 1997, applicants submit attached Form PTO-1449 and a copy of the references listed on the attached form PTO-1449 including a copy of an English translation of the following foreign language references, or portions thereof:

Desig. ID	Source
AI	<p>Japan 59-166984: Concise Statement – Japanese Laid-Open 59-166984 Publication Date: September 20, 1984</p> <p>1. Title of the Invention METHOD FOR FABRICATING MATRIX LIQUID CRYSTAL DISPLAY DEVICE</p> <p>2. Claim</p> <p>1. A method for fabricating a matrix liquid crystal display device, comprising the steps of: forming lead electrodes of gate lines and source lines of a Mos transistor at the periphery of a first substrate in which unit elements including the Mos transistor are arranged in matrix and the gate lines and th source lines (or the drain lines) of the transistor are arranged in grid; aligning liquid crystal over the first substrate; sandwiching the liquid crystal between the first substrate and a second substrate having a transparent conductive film; and shorting the gate lines and the source lines (or the drain lines) at least during the step of aligning the liquid crystal.</p> <p>2. A method for fabricating a matrix liquid crystal display device according to Claim 1, wherein the gate lines and the source lines (or the drain lines) are shorted by shorting the lead electrodes of the gate lines and the source lines (or the drain lines).</p> <p>3. A method for fabricating the matrix liquid crystal display device according to Claim 1 or Claim 2, wherein the gate lines and the source lines (or the drain lines) are shorted by a short ring which shorts the gate lines and the source lines (or the drain lines) and is provided over the first substrate.</p>
AJ	<p>Japan 6-258659: Patent Abstract; 2004 JPO & JAPIO; File 347; No. 04586759 and English Abstract; DIALOG(R) File 352: Derwent WPI (2004); Accession No.</p>
AK	<p>Japan 6-186578: Patent Abstract; 2004 JPO & JAPIO; File 347; No. 04715578 and English Abstract; DIALOG(R) File 352: Derwent WPI (2004); Accession No.</p>
AL	<p>Japan 6-258660: Patent Abstract; 2004 JPO & JAPIO; File 347; No. 04586760 and English Abstract; DIALOG(R) File 352: Derwent WPI (2004); Accession No.</p>

This statement is being filed after an first Office action improperly designated as a Final Office Action. This statement is being filed concurrently with a response to that Office Action mailed on March 25, 2004, which requests withdrawal of the finality of the office action. Since the finality of the office action is improper, no statement under 1.97(e) is required. A check for

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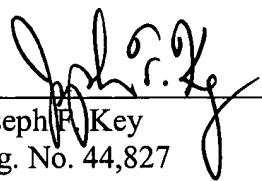
Attorney's Docket No.: 07977-105001 / US3189

\$180 in payment of the fee set forth in §1.17(p) is enclosed. Please apply any other deficiencies or credits to Deposit Account No. 06-1050.

Respectfully submitted,

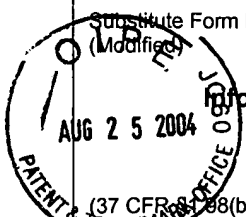
Date: _____

8/25/2004



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 Substitute Form PTO-1449 (Modified) Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR 1.98(b))	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 07977-105001	Application No. 08/770,792
	Applicant Jun Koyama, et al.		
	Filing Date December 19, 1996	Group Art Unit 2871	

U.S. Patent Documents

Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA	6 304 243	Oct 16, 2001	Kondo et al.			
	AB						
	AC						

Foreign Patent Documents or Published Foreign Patent Applications

Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
	AD	53-29098	17 Mar 1978	Japan			ABS	
	AE	57-32419	22 Feb 1982	Japan			ABS	
	AF	59-214075	Dec 3, 1984	Japan			ABS	
	AG	61-29821	10 Feb 1986	Japan			ABS	
	AH	61-177481	9 Aug 1986	Japan			ABS	
	AI	4-13116	17 Jan 1992	Japan			ABS	
	AJ	4-116625	17 Apr 1992	Japan			ABS	
	AK	0 474 508	11 Mar 1992	Europe				
	AL	4-352131	7 Dec 1992	Japan			ABS	
	AM	4-362921	15 Dec 1992	Japan			ABS	
	AN	6-186580	8 Jul 1994	Japan			ABS	
	AO	6-202160	22 Jul 1994	Japan			ABS	
	AP	0 593 266	20 Apr 1994	Europe				
	AQ	6-289414	18 Oct 1994	Japan			ABS	
	AR	7-218883	18 Aug 1995	Japan			ABS	

Other Documents (include Author, Title, Date, and Place of Publication)

Examiner Initial	Desig. ID	Document
	AS	
	AT	
	AU	

Examiner Signature	Date Considered
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	

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	AA						
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							Yes	No
	AI	59-166984	09/20/1984	Japan			Concise Statement	
	AJ	6-258659	09/16/1994	Japan			Abst	
	AK	6-186578	07/08/1994	Japan			Abst	
	AL	6-258660	09/16/1994	Japan			Abst	
	AM							

Other Documents (include Author, Title, Date, and Place of Publication)

Examiner Initial	Desig. ID	Document
	AN	
	AO	
	AP	
	AQ	

Examiner Signature	Date Considered
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	